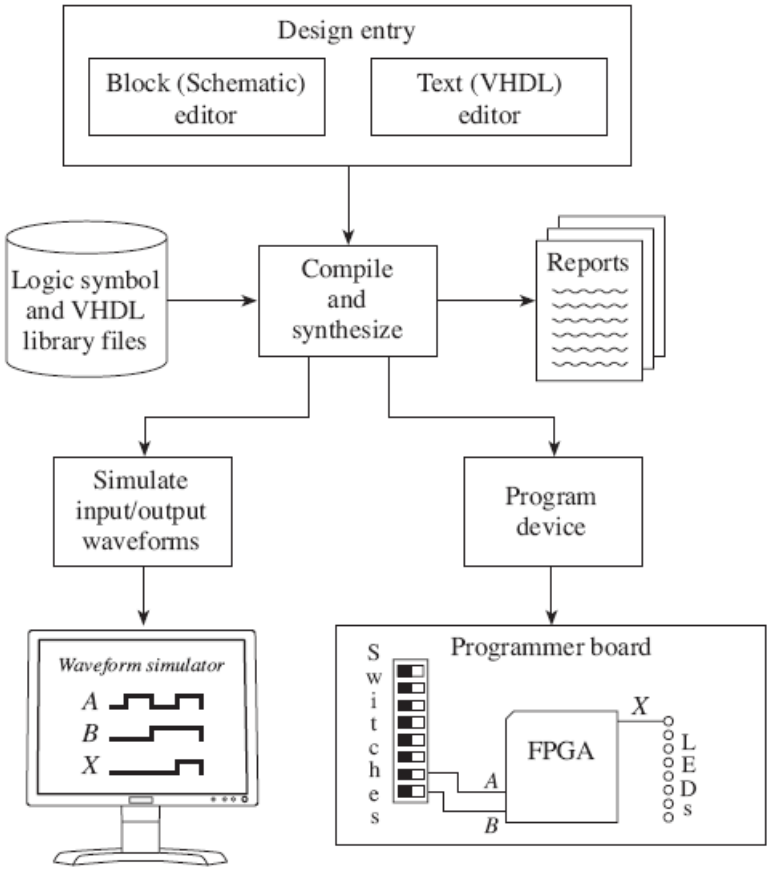
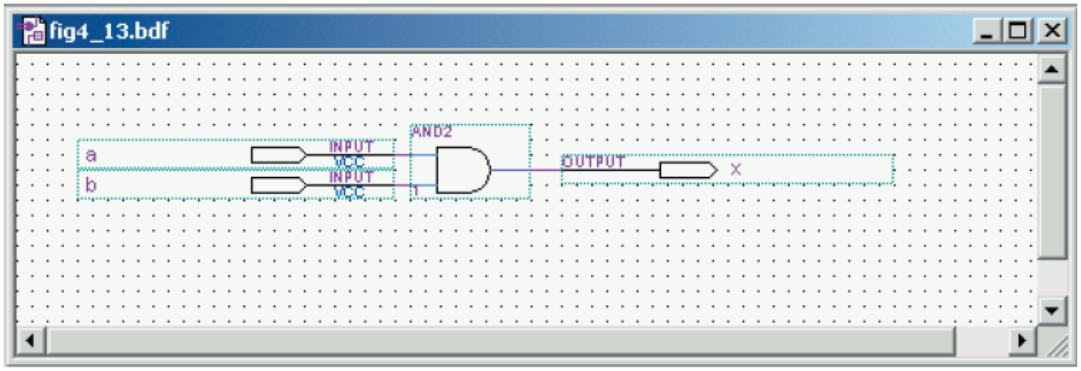
FPGA i VHDL

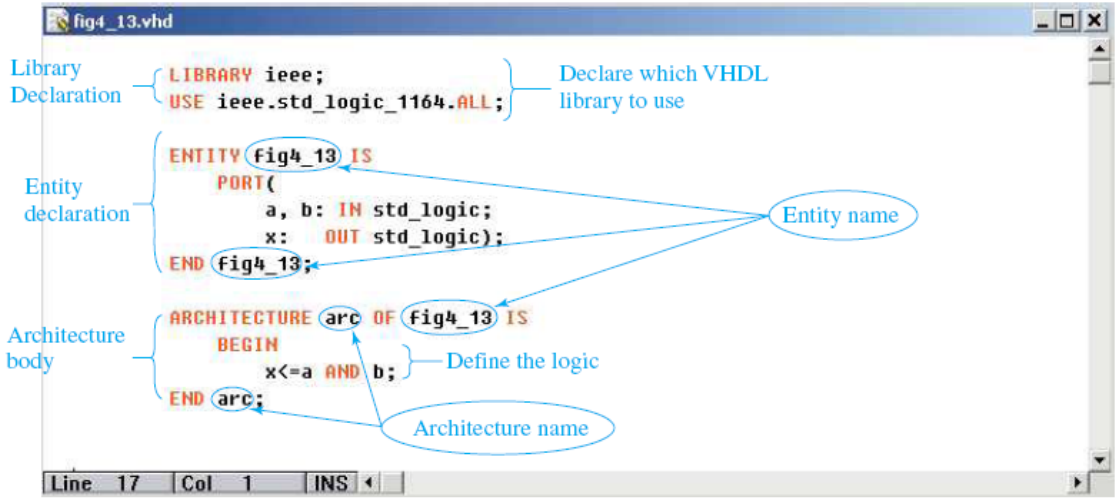
Od dizajna do realizacije



VHDL primer



(a)



Altera Quartus Tutorial

- Pročitati:
  - Digital Electronics – A practical Approach With VHDL 9th edition pp. 126-155.

Kombinaciona kola i minimizacija

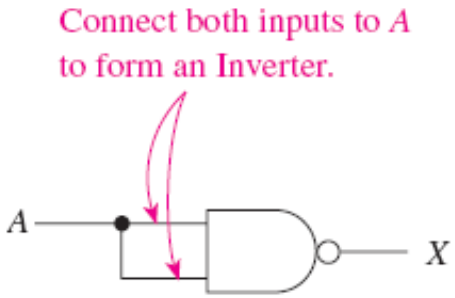
- Pročitati:
  - Digital Electronics – A practical Approach With VHDL 9th edition, Poglavlje 5, sa fokusom na 5-6, 5-7, 5-8 i 5-9.

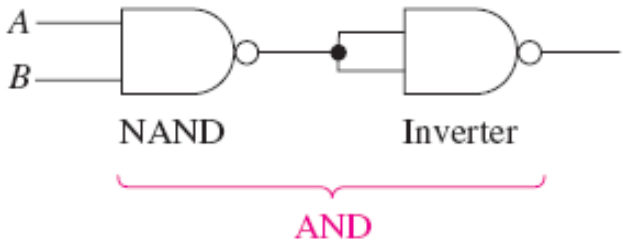
Primer

- Modelovati ovu tabelu korišćenjem VHDL-a:

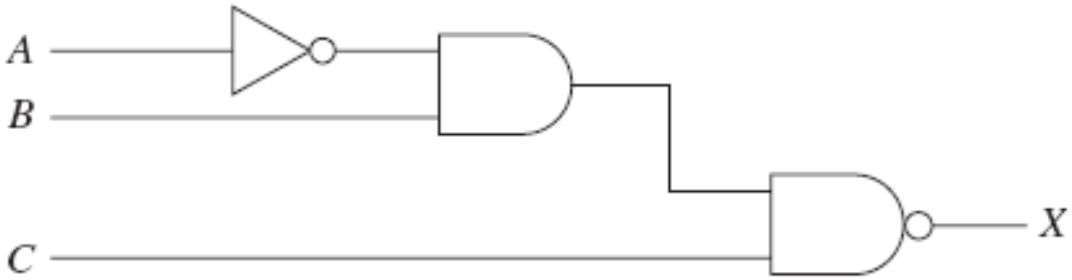
Inputs			Output
A	*B*	*C*	*X*
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

Univerzalna logička kola NAND i NOR



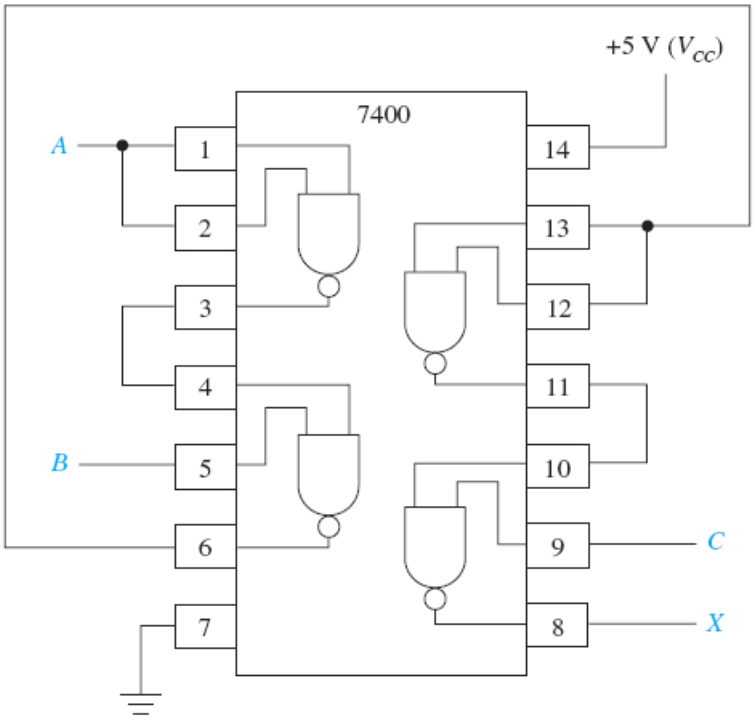


Kako OR?
Kako NOR?



Kako ovo napraviti samo pomoću NAND-ova?

7400 TTL IC



Domaći

- Za domaći sve ovo preko NOR-ova?
  - Inverter
  - OR
  - AND
  - NAND
- Napraviti blok dijagrame i VHDL programe.